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a driver circuit comprising thin film transistors formed over said substrate, a resin, adhering said thin film transistors to said substrate and a metal bump electrically coupling said driver circuit to said pixel circuit.

REMARKS

Reconsideration and allowance of the above-referenced application are respectfully requested.

Claims 1-62 stand rejected under 35 USC 102(e) as allegedly being anticipated by U.S. Patent No. 5,757,456. Applicants respectfully submit that U.S. Patent No. 5,757,456 doe NOT constitute a reference under 35 USC 102(e) because the present application claims the priority benefits from Japanese applications JP7-86372 filed on March 18, 1995 and JP 7-88759 filed on March 22, 1995, respectively, both of which predate the U.S. filing date of March 8, 1996 of the U.S. Patent No. 5,757,456. See the verified English translations of Japanese applications JP7-86372 and JP 7-88759 filed concurrently with this amendment. Hence, the pending claims are patentable under 35 USC 102(e).

With respect to the double patenting rejection over claims 5 and 17 of the parent case issued as U.S. Patent No. 5,834,327, Applicants respectfully traverse the contention of the Patent Office. The claimed invention of the present case can be

achieved by techniques other than what is claimed in Claims 5 and 17 of the '327 patent. The Office Action fails to show how the Claims 5 and 17 of the '327 patent make the pending claims obvious. Hence, it is respectfully suggested that the rejections be withdrawn.

In view of the above amendments and remarks, all of the claims should be in full condition for allowance. A formal notice to that effect is respectfully solicited.

If there are any other charges, or any credits, please apply them to Deposit Account No. 06-1050.

Respectfully submitted,

Date: 6/6/00



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